

### P-channel -40V, DFN5\*6 MOSFET P-溝道場效應管

#### ■ Features 特點

Low on-resistance 低導通電阻

Advanced trench technology 優秀溝槽技術

Backside heat sink 背面熱沈

$R_{DS(ON)} \leq 13\text{m}\Omega @ VGS = -10\text{V}$

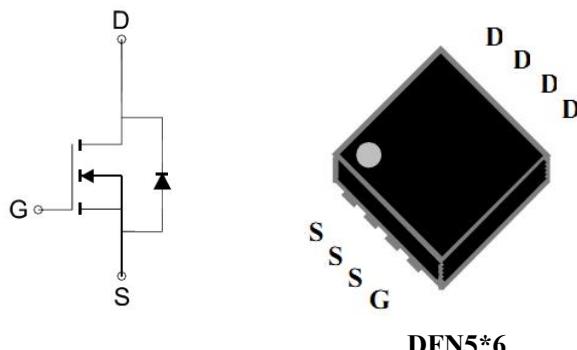
$R_{DS(ON)} \leq 22\text{m}\Omega @ VGS = -4.5\text{V}$

#### ■ Applications 應用

DC/DC Converter 直流/直流變換

Ideal for high-frequency switching Synchronous rectification 高頻開關同步整流的理想選擇

#### ■ Internal Schematic Diagram 內部結構



#### ■ Absolute Maximum Ratings 最大額定值

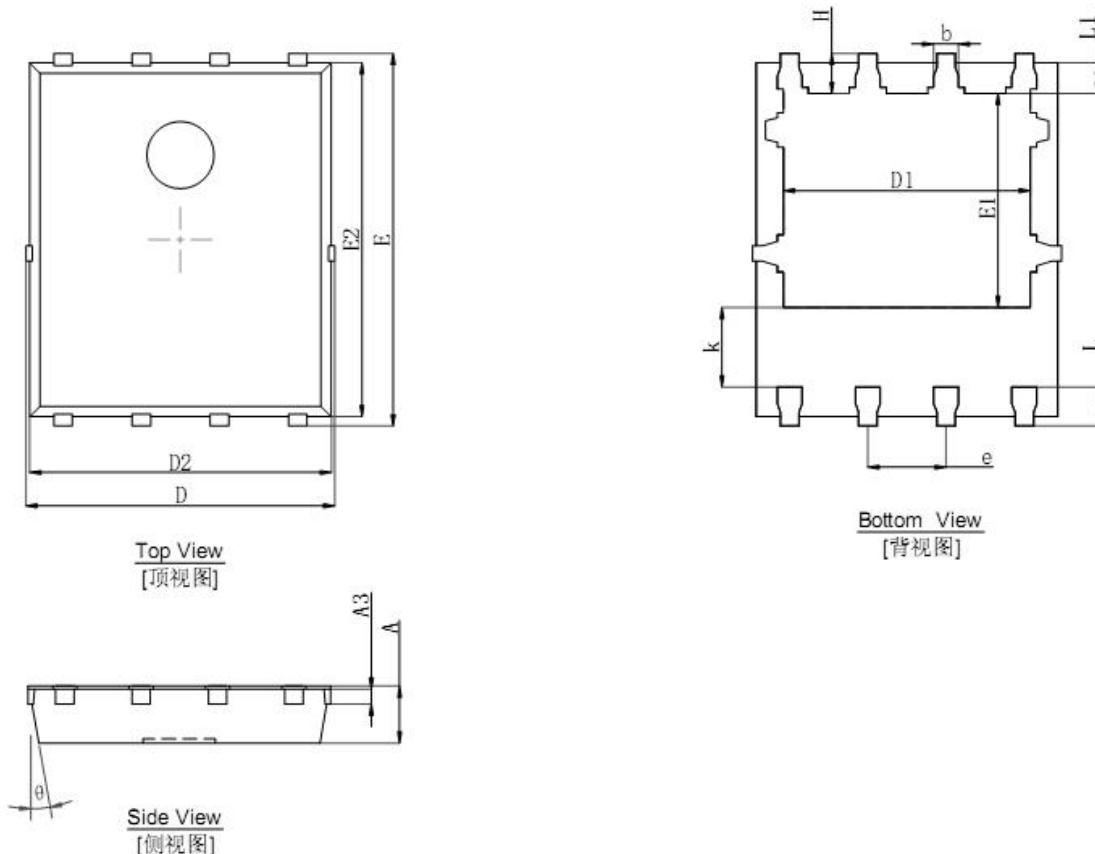
Characteristic 特性參數	Symbol 符號	Rating 額定值	Unit 單位
Drain-Source Voltage 漏極-源極電壓	$BV_{DSS}$	-40	V
Gate- Source Voltage 柵極-源極電壓	$V_{GS}$	$\pm 20$	V
Drain Current (continuous)漏極電流-連續	$I_D$	-40	A
Drain Current (pulsed)漏極電流-脈沖	$I_{DM}$	-100	A
Total Device Dissipation 總耗散功率	$P_{TOT}(\text{at } T_C = 25^\circ\text{C})$	35	W
Thermal Resistance Junction-Ambient 热阻	$R_{\theta JA}$	3.6	$^\circ\text{C}/\text{W}$
Junction/Storage Temperature 結溫/儲存溫度	$T_J, T_{stg}$	-55~150	$^\circ\text{C}$

## ■ Electrical Characteristics 電特性

( $T_A=25^\circ\text{C}$  unless otherwise noted 如無特殊說明，溫度為  $25^\circ\text{C}$ )

Characteristic 特性參數	Symbol 符號	Min 最小值	Typ 典型值	Max 最大值	Unit 單位
Drain-Source Breakdown Voltage 漏極-源極擊穿電壓( $I_D = -250\mu\text{A}, V_{GS} = 0\text{V}$ )	$\text{BV}_{\text{DSS}}$	-40	—	—	V
Gate Threshold Voltage 柵極開啓電壓( $I_D = -250\mu\text{A}, V_{GS} = V_{DS}$ )	$V_{GS(\text{th})}$	-1.0	-1.7	-2.5	V
Zero Gate Voltage Drain Current 零柵壓漏極電流( $V_{GS} = 0\text{V}, V_{DS} = -40\text{V}$ )	$I_{\text{DSS}}$	—	—	-1	$\mu\text{A}$
Gate Body Leakage 柵極漏電流( $V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$ )	$I_{\text{GSS}}$	—	—	$\pm 100$	nA
Static Drain-Source On-State Resistance 靜態漏源導通電阻( $I_D = -20\text{A}, V_{GS} = -10\text{V}$ ) ( $I_D = -10\text{A}, V_{GS} = -4.5\text{V}$ )	$R_{\text{DS}(\text{ON})}$	—	10 15	13 22	$\text{m}\Omega$
Diode Forward Voltage Drop 內附二極管正向壓降( $I_{SD} = -20\text{A}, V_{GS} = 0\text{V}$ )	$V_{SD}$	—	—	-1.2	V
Input Capacitance 輸入電容 ( $V_{GS} = 0\text{V}, V_{DS} = -20\text{V}, f = 1\text{MHz}$ )	$C_{\text{ISS}}$	—	3030	—	pF
Common Source Output Capacitance 共源輸出電容( $V_{GS} = 0\text{V}, V_{DS} = -20\text{V}, f = 1\text{MHz}$ )	$C_{\text{OSS}}$	—	362	—	pF
Reverse Transfer Capacitance 反向傳輸電容 ( $V_{GS} = 0\text{V}, V_{DS} = -20\text{V}, f = 1\text{MHz}$ )	$C_{\text{RSS}}$	—	315	—	pF
Gate Source Charge 柵源電荷密度 ( $V_{DS} = -20\text{V}, I_D = -20\text{A}, V_{GS} = -10\text{V}$ )	$Q_{\text{gs}}$	—	7.8	—	nC
Gate Drain Charge 柵漏電荷密度 ( $V_{DS} = -20\text{V}, I_D = -20\text{A}, V_{GS} = -10\text{V}$ )	$Q_{\text{gd}}$	—	10.3	—	nC
Turn-On Delay Time 開啓延遲時間 ( $V_{DS} = -20\text{V}, I_D = -20\text{A}, R_{\text{GEN}} = 3\Omega, V_{GS} = -10\text{V}$ )	$t_{d(\text{on})}$	—	13	—	ns
Turn-On Rise Time 開啓上升時間 ( $V_{DS} = -20\text{V}, I_D = -20\text{A}, R_{\text{GEN}} = 3\Omega, V_{GS} = -10\text{V}$ )	$t_r$	—	22	—	ns
Turn-Off Delay Time 關斷延遲時間 ( $V_{DS} = -20\text{V}, I_D = -20\text{A}, R_{\text{GEN}} = 3\Omega, V_{GS} = -10\text{V}$ )	$t_{d(\text{off})}$	—	39	—	ns
Turn-On Fall Time 開啓下降時間 ( $V_{DS} = -20\text{V}, I_D = -20\text{A}, R_{\text{GEN}} = 3\Omega, V_{GS} = -10\text{V}$ )	$t_f$	—	29.8	—	ns

■DIMENSION 外形封裝尺寸



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°		12°	