

P-channel -60V, -13A, TO-251 Power MOSFET 功率場效應管

■Features 特點

Ultra low on-resistance 超低導通電阻

Low gate charge 低柵電荷密度

Fast switching 快速開關能力

■Applications 應用

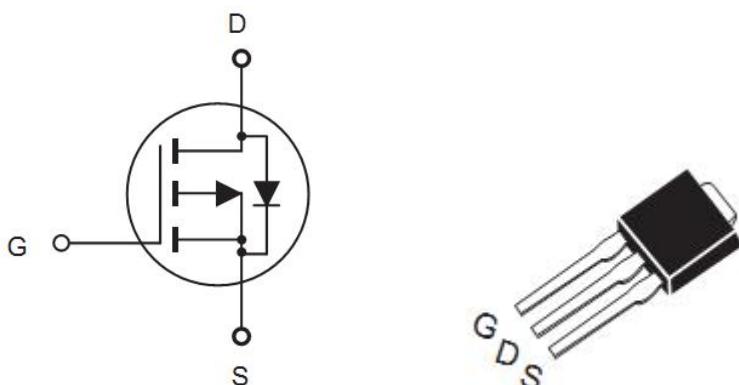
Switch mode power supplies 開關電源

DC-DC converters and UPS 直流直流變換和不斷電源

PWM motor controls 脉寬調制電機控制

General switching applications 普通開關應用

■Internal Schematic Diagram 內部結構



■Absolute Maximum Ratings 最大額定值

| Characteristic 特性參數 | Symbol 符號 | Rat 額定值 | Unit 單位 |
|--|-----------------------------------|----------|--------------|
| Drain-Source Voltage 漏極-源極電壓 | BV_{DSS} | -60 | V |
| Gate- Source Voltage 柵極-源極電壓 | V_{GS} | ± 20 | V |
| Drain Current (continuous)漏極電流-連續 | I_D (at $TC = 25^\circ C$) | -13 | A |
| Drain Current (pulsed)漏極電流-脉冲 | I_{DM} | -52 | A |
| Total Device Dissipation 總耗散功率 | P_{TOT} (at $TC = 25^\circ C$) | 42 | W |
| Thermal Resistance Junction-Ambient 热阻 | $R_{\Theta JA}$ | 50 | $^\circ C/W$ |
| Junction/Storage Temperature 結溫/儲存溫度 | T_J, T_{stg} | -55~150 | $^\circ C$ |

■ Electrical Characteristics 電特性

($T_A=25^\circ\text{C}$ unless otherwise noted 如無特殊說明，溫度為 25°C)

| Characteristic 特性參數 | Symbol 符號 | Min 最小值 | Typ 典型值 | Max 最大值 | Unit 單位 |
|--|---------------------|------------|------------|------------|------------------|
| Drain-Source Breakdown Voltage 漏極-源極擊穿電壓($I_D = -250\mu\text{A}, V_{GS} = 0\text{V}$) | BV_{DSS} | -60 | — | — | V |
| Gate Threshold Voltage 柵極開關電壓($I_D = -250\mu\text{A}, V_{GS} = V_{DS}$) | $V_{GS(\text{th})}$ | -1 | -2 | -3 | V |
| Zero Gate Voltage Drain Current 零柵壓漏極電流($V_{GS} = 0\text{V}, V_{DS} = -48\text{V}$) | I_{DSS} | — | — | -1 | μA |
| Gate Body Leakage 柵極漏電流($V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$) | I_{GSS} | — | — | ± 100 | nA |
| Static Drain-Source On-State Resistance 靜態漏源導通電阻($I_D = -9\text{A}, V_{GS} = -10\text{V}$) ($I_D = -7\text{A}, V_{GS} = -4.5\text{V}$) | $R_{DS(\text{ON})}$ | — | 105 140 | 125 175 | $\text{m}\Omega$ |
| Source Drain Current 源極-漏極電流 | I_{SD} | — | — | -14 | A |
| Diode Forward Voltage Drop 內附二極管正向壓降($I_{SD} = -14\text{A}, V_{GS} = 0\text{V}$) | V_{SD} | — | — | -2 | V |
| Input Capacitance 輸入電容 ($V_{GS} = 0\text{V}, V_{DS} = -30\text{V}, f = 1\text{MHz}$) | C_{ISS} | — | 615 | — | pF |
| Common Source Output Capacitance 共源輸出電容($V_{GS} = 0\text{V}, V_{DS} = -30\text{V}, f = 1\text{MHz}$) | C_{OSS} | — | 140 | — | pF |
| Total Gate Charge 總柵極電荷密度 ($V_{DS} = -30\text{V}, I_D = -3.7\text{A}, V_{GS} = -10\text{V}$) | Q_g | — | 5 | — | nC |
| Gate Source Charge 柵源電荷密度 ($V_{DS} = -30\text{V}, I_D = -3.7\text{A}, V_{GS} = -10\text{V}$) | Q_{gs} | — | 2 | — | nC |
| Gate Drain Charge 柵漏電荷密度 ($V_{DS} = -30\text{V}, I_D = -3.7\text{A}, V_{GS} = -10\text{V}$) | Q_{gd} | — | 4 | — | nC |
| Turn-On Delay Time 開啟延遲時間 ($V_{DS} = -30\text{V}, I_D = -1\text{A}, R_{GEN} = 6\Omega, V_{GS} = -10\text{V}$) | $t_{d(on)}$ | — | 11 | — | ns |
| Turn-On Rise Time 開啟上升時間 ($V_{DS} = -30\text{V}, I_D = -1\text{A}, R_{GEN} = 6\Omega, V_{GS} = -10\text{V}$) | t_r | — | 4.5 | — | ns |
| Turn-Off Delay Time 關斷延遲時間 ($V_{DS} = -30\text{V}, I_D = -1\text{A}, R_{GEN} = 6\Omega, V_{GS} = -10\text{V}$) | $t_{d(off)}$ | — | 50 | — | ns |
| Turn-On Fall Time 開啟下降時間 ($V_{DS} = -30\text{V}, I_D = -1\text{A}, R_{GEN} = 6\Omega, V_{GS} = -10\text{V}$) | t_f | — | 15 | — | ns |

■ **TYPICAL CHARACTERISTIC CURVE** 典型特性曲线

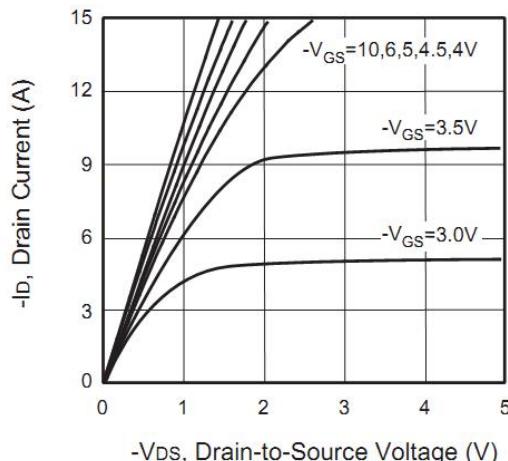


Figure 1. Output Characteristics

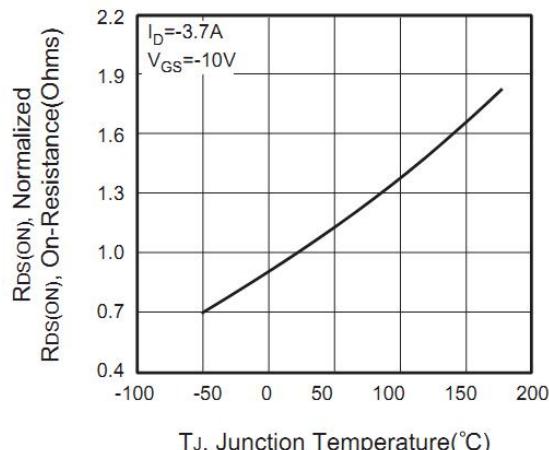


Figure 2. On-Resistance Variation with Temperature

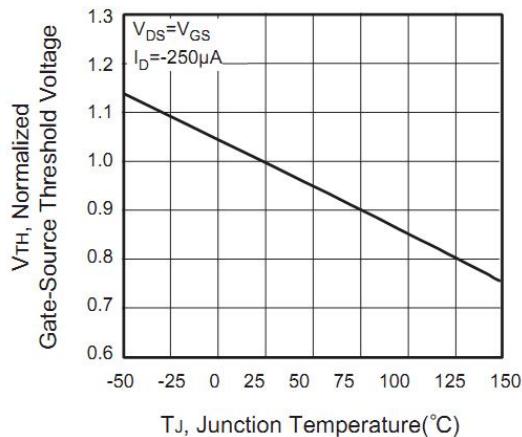


Figure 3. Gate Threshold Variation with Temperatures

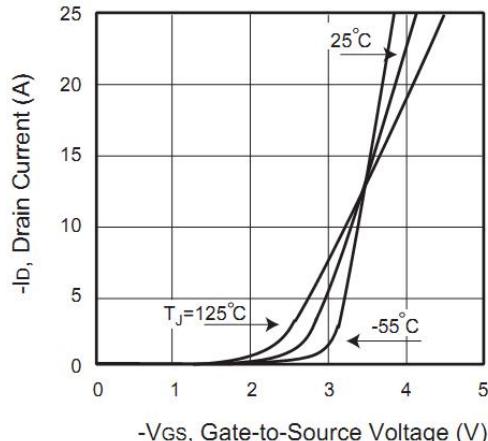


Figure 4. Transfer Characteristics

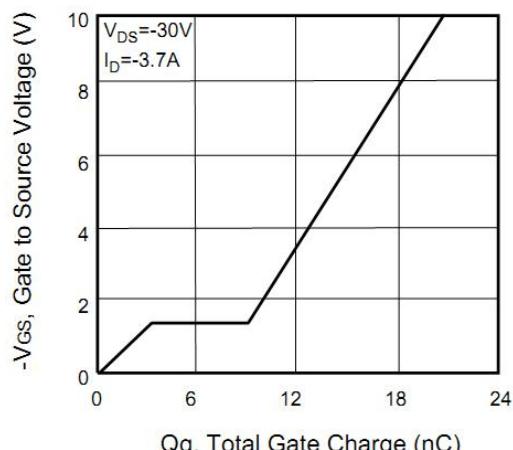


Figure 5. Gate charge VS. Gate-source Voltage

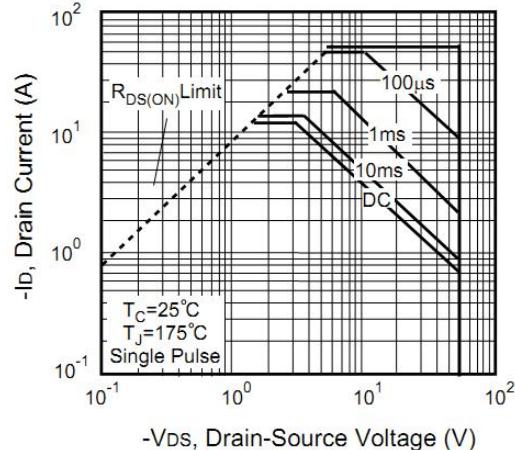


Figure 6. Maximum Safe Operating Area



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GMD13P06

■TO-251 DIMENSION 外形封裝尺寸

單位(UNIT): mm

